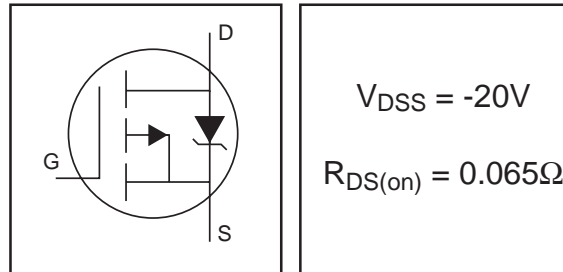




IRLML6402

Power MOSFET

- Ultra Low On-Resistance
- P-Channel MOSFET
- SOT-23 Footprint
- Low Profile (<1.1mm)
- Available in Tape and Reel
- Fast Switching



Description

These P-Channel MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in battery and load management.

A thermally enhanced large pad leadframe has been incorporated into the standard SOT-23 package to produce a Power MOSFET with the industry's smallest footprint. This package, , is ideal for applications where printed circuit board space is at a premium. The low profile (<1.1mm) of the allows it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards. The thermal resistance and power dissipation are the best available.

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain- Source Voltage	-20	V
$I_D @ T_A = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ -4.5\text{V}$	-3.7	A
I_{DM}	Pulsed Drain Current ①	-22	
$P_D @ T_A = 25^\circ\text{C}$	Power Dissipation	1.3	W
$P_D @ T_A = 70^\circ\text{C}$	Power Dissipation	0.8	
	Linear Derating Factor	0.01	W/°C
E_{AS}	Single Pulse Avalanche Energy④	11	mJ
V_{GS}	Gate-to-Source Voltage	± 12	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

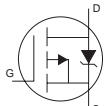
	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient③	75	100	°C/W



Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	-0.009	—	V/°C	Reference to 25°C, I _D = -1mA ②
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	0.050	0.065	Ω	V _{GS} = -4.5V, I _D = -2.3A ②
		—	0.080	0.135		V _{GS} = -2.5V, I _D = -2.0A ②
V _{GS(th)}	Gate Threshold Voltage	-0.40	-0.55	-1.2	V	V _{DS} = V _{GS} , I _D = -250μA
g _{fs}	Forward Transconductance	6.0	—	—	S	V _{DS} = -10V, I _D = -2.5A ②
I _{DSS}	Drain-to-Source Leakage Current	—	—	-1.0	μA	V _{DS} = -20V, V _{GS} = 0V
I _{GSS}	Gate-to-Source Forward Leakage	—	—	-100	nA	V _{GS} = -12V
	Gate-to-Source Reverse Leakage	—	—	100		V _{GS} = 12V
Q _g	Total Gate Charge	—	8.0	12	nC	I _D = -3.7A
Q _{gs}	Gate-to-Source Charge	—	1.2	1.8		V _{DS} = -10V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	2.8	4.2		V _{GS} = -5.0V ②
t _{d(on)}	Turn-On Delay Time	—	350	—	ns	V _{DD} = -10V
t _r	Rise Time	—	48	—		I _D = -3.7A
t _{d(off)}	Turn-Off Delay Time	—	588	—		R _G = 89Ω
t _f	Fall Time	—	381	—		R _D = 2.7Ω
C _{iss}	Input Capacitance	—	633	—	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	145	—		V _{DS} = -10V
C _{rss}	Reverse Transfer Capacitance	—	110	—		f = 1.0MHz

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	-1.3	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	-22		
V _{SD}	Diode Forward Voltage	—	—	-1.2	V	T _J = 25°C, I _S = -1.0A, V _{GS} = 0V ②
t _{rr}	Reverse Recovery Time	—	29	43	ns	T _J = 25°C, I _F = -1.0A
Q _{rr}	Reverse Recovery Charge	—	11	17	nC	di/dt = -100A/μs ②

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width ≤ 400μs; duty cycle ≤ 2%.
- ③ Surface mounted on 1" square single layer 1oz. copper FR4 board, steady state.
- ④ Starting T_J = 25°C, L = 1.65mH
R_G = 25Ω, I_{AS} = -3.7A.

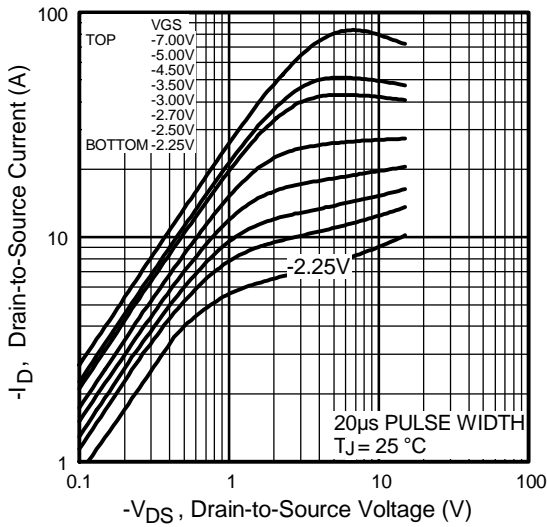


Fig 1. Typical Output Characteristics

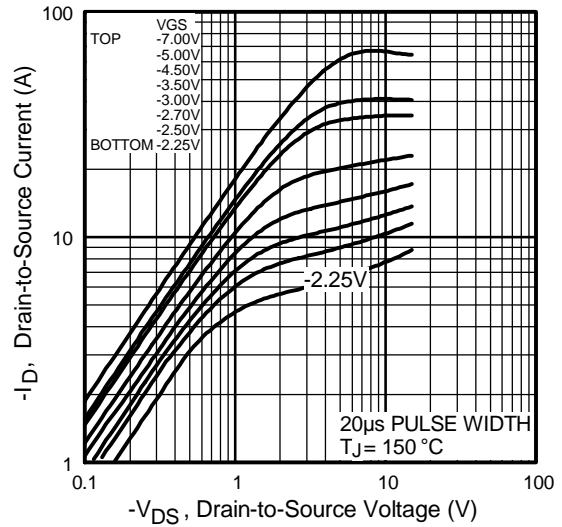


Fig 2. Typical Output Characteristics

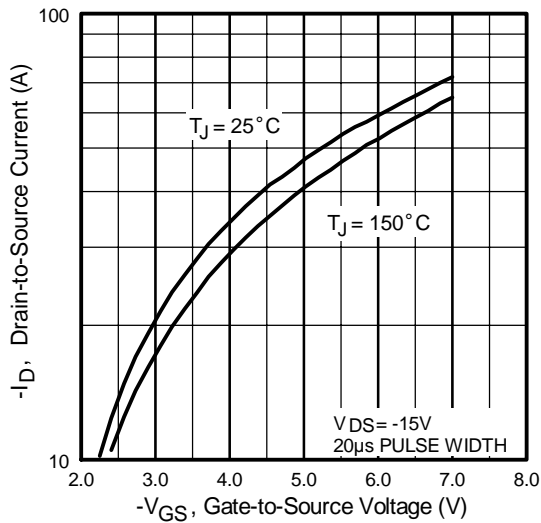


Fig 3. Typical Transfer Characteristics

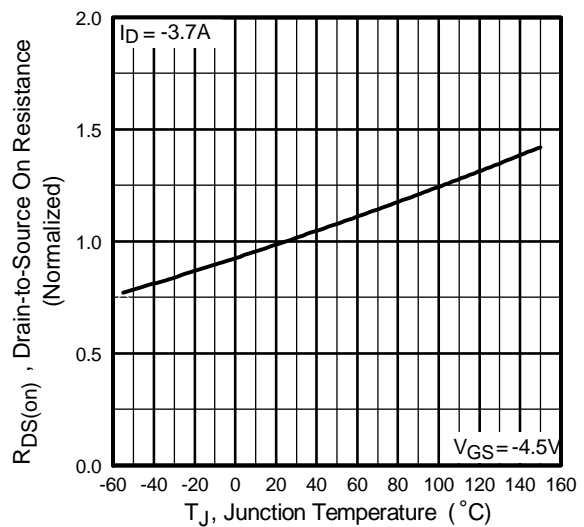


Fig 4. Normalized On-Resistance Vs. Temperature

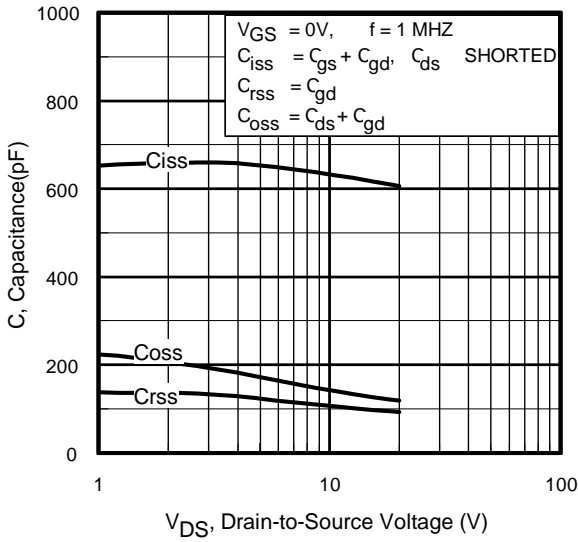


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

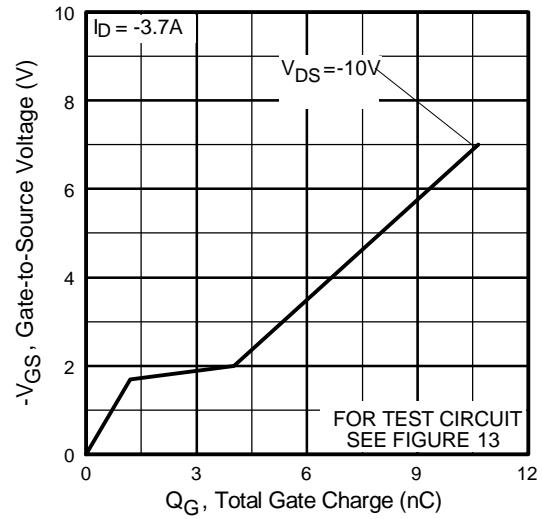


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

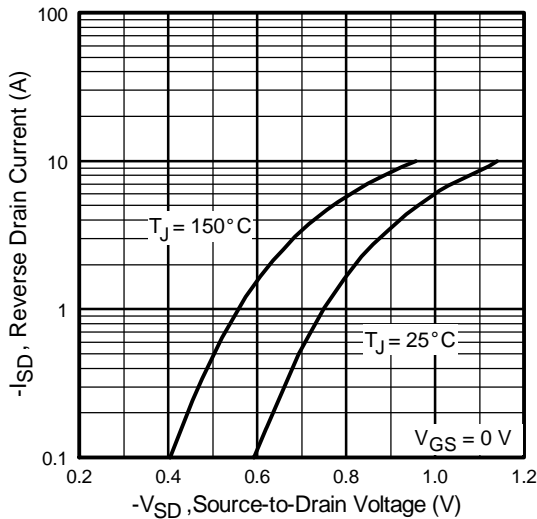


Fig 7. Typical Source-Drain Diode Forward Voltage

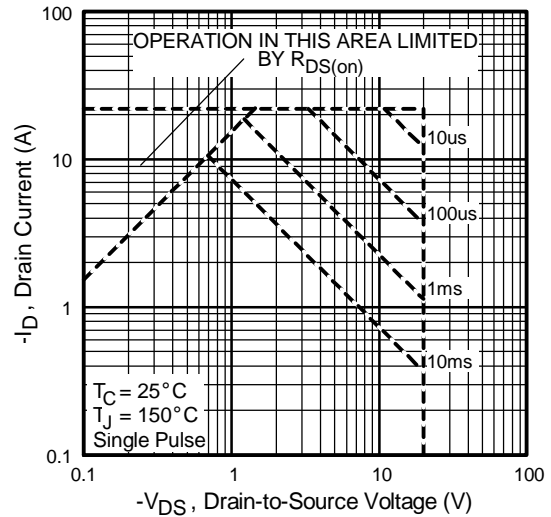


Fig 8. Maximum Safe Operating Area

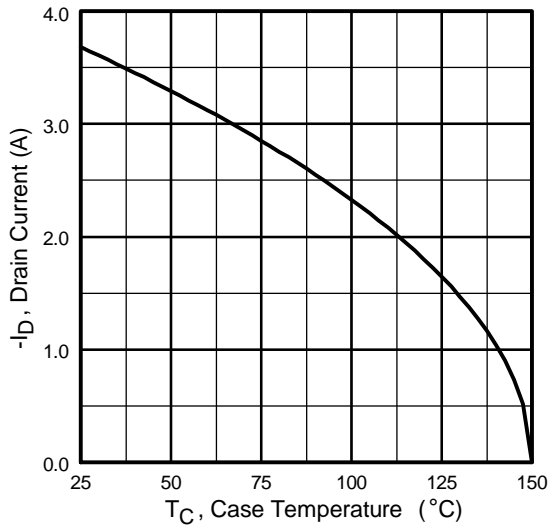


Fig 9. Maximum Drain Current Vs. Case Temperature

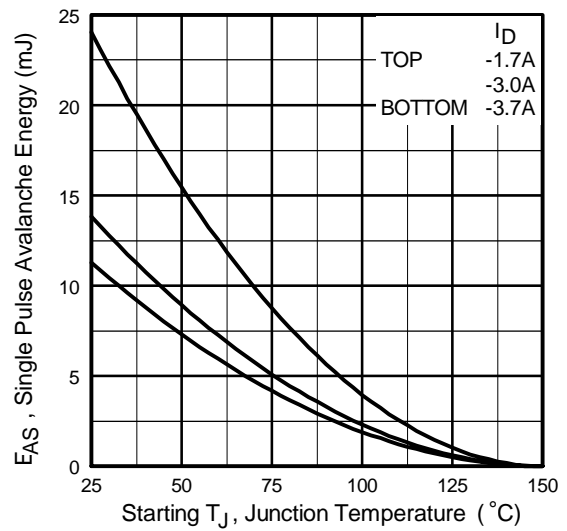


Fig 10. Maximum Avalanche Energy Vs. Drain Current

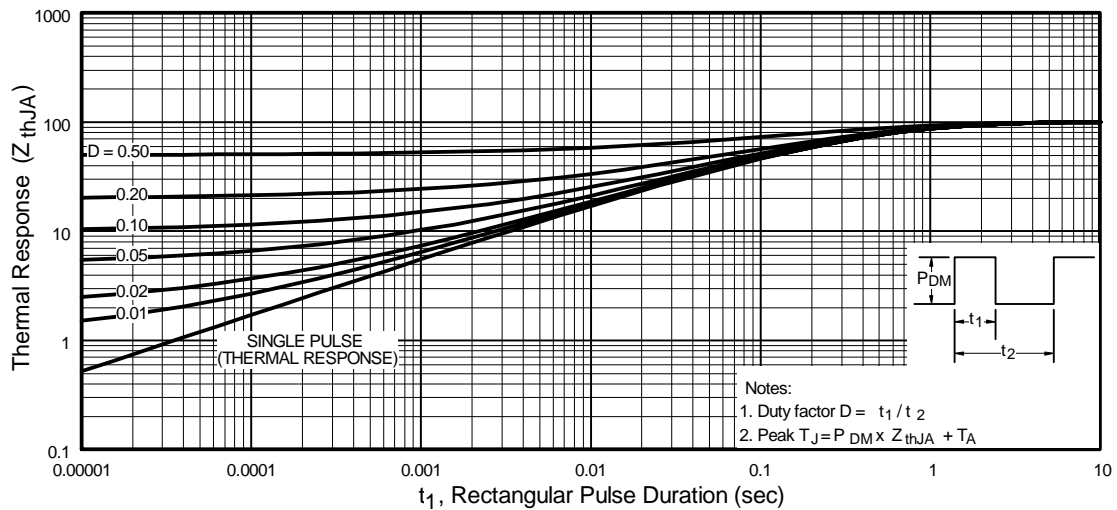


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

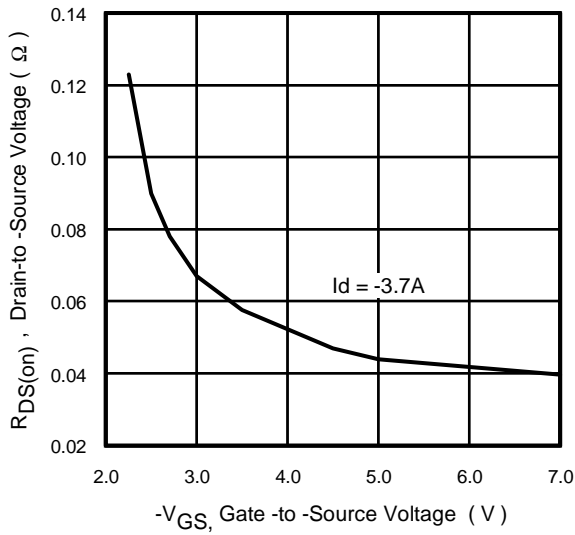


Fig 12. Typical On-Resistance Vs. Gate Voltage

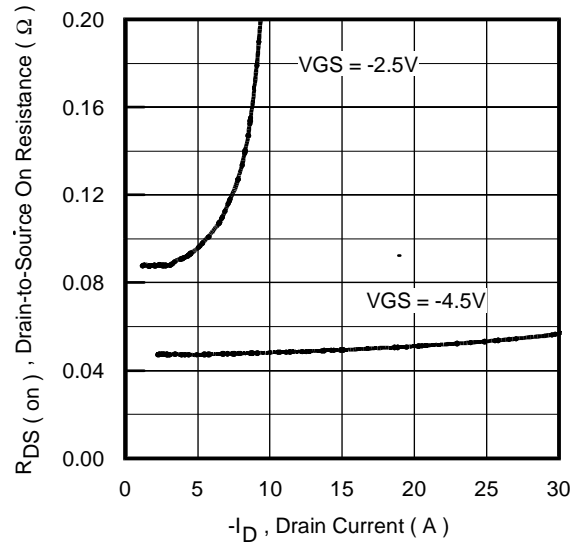


Fig 13. Typical On-Resistance Vs. Drain Current