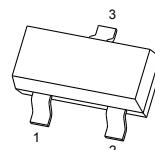
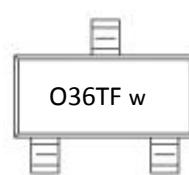


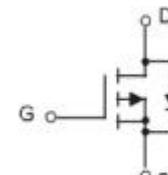
**TF2333 P-Channel 20-V(D-S) MOSFET**

<b>V<sub>(BR)DSS</sub></b>	<b>R<sub>D(on)TYP</sub></b>	<b>I<sub>D</sub></b>
<b>-20V</b>	0.022Ω@-4.5V	<b>-6.0A</b>
	0.027Ω@-2.5V	
	0.045Ω@-1.8V	

**SOT-23**

 1.GATE  
2.SOURCE  
3.DRAIN

**MARKING**


\*w: week code

**Equivalent Circuit**

**General FEATURE**

- TrenchFET Power MOSFET
- Lead free product is acquired
- Surface mount package

**APPLICATION**

- Load Switch for Portable Devices
- DC/DC Converter

**Maximum ratings (T<sub>a</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	
Continuous Drain Current	I <sub>D</sub>	-6.0	A
Pulsed Drain Current	I <sub>DM</sub>	-20	
Continuous Source-Drain Diode Current	I <sub>S</sub>	-1.4	
Maximum Power Dissipation	P <sub>D</sub>	1.4	W
Thermal Resistance from Junction to Ambient(t ≤5s)	R <sub>θJA</sub>	69	°C/W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 ~+150	



SHENZHEN TUOFENG SEMICONDUCTOR TECHNOLOGY CO.,LTD

## SOT-23 / SOT-23-3L Plastic-Encapsulate MOSFETs

TF2333

## MOSFET ELECTRICAL CHARACTERISTICS

Ta = 25 °C unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Static</b>						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-20			V
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.4	-0.7	-1	
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±10 V			±100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -12V, V <sub>GS</sub> = 0V			-1	μA
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -6.0A		0.022	0.026	Ω
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -5.0A		0.027	0.035	
		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -3.0A		0.045	0.050	
Forward transconductance <sup>a</sup>	g <sub>f</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -5.0A		17.0	-	S
<b>Dynamic<sup>b</sup></b>						
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -6V, V <sub>GS</sub> = 0V, f = 1MHz		1100		pF
Output capacitance	C <sub>oss</sub>			390		
Reverse transfer capacitance	C <sub>rss</sub>			300		
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> = -6V, V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -5.0A		11.5		nC
Gate-source charge	Q <sub>gs</sub>			1.5		
Gate-drain charge	Q <sub>gd</sub>			3.2		
Gate resistance	R <sub>g</sub>	f = 1MHz	1.9		19	Ω
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = -6V, I <sub>D</sub> = -4.0A R <sub>L</sub> = 6Ω, V <sub>GEN</sub> = -4.5V, R <sub>g</sub> = 6Ω		25.0		ns
Rise time	t <sub>r</sub>			45.0		
Turn-off delay time	t <sub>d(off)</sub>			72.0		
Fall time	t <sub>f</sub>			60.0		
<b>Drain-source body diode characteristics</b>						
Continuous source-drain diode current	I <sub>s</sub>	T <sub>c</sub> = 25°C			-1.0	A
Pulse diode forward current <sup>a</sup>	I <sub>SM</sub>				-20	
Body diode voltage	V <sub>SD</sub>	I <sub>s</sub> = -1.0A		-0.8	-1.2	V

**Notes :**

a. Pulse Test : Pulse Width &lt; 300μs, Duty Cycle ≤ 2%.

b. Guaranteed by design, not subject to production testing.

### Typical Electrical and Thermal Characteristics

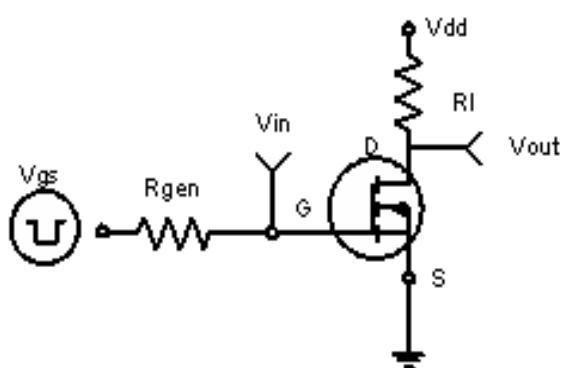


Figure 1:Switching Test Circuit

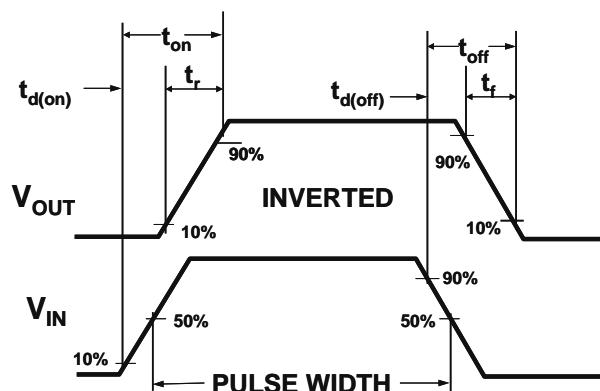
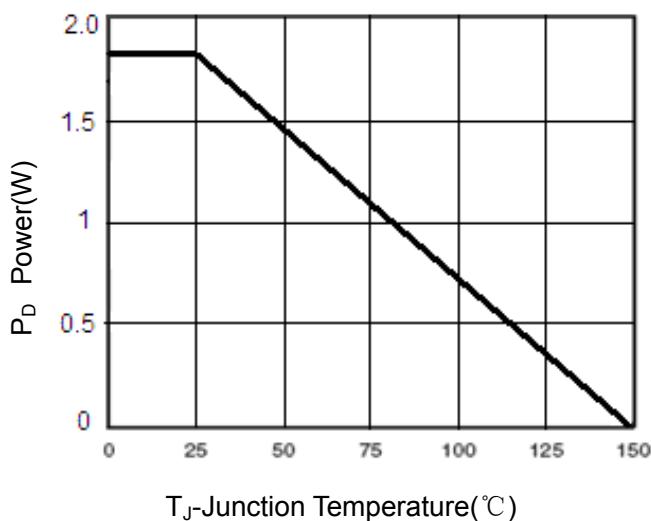
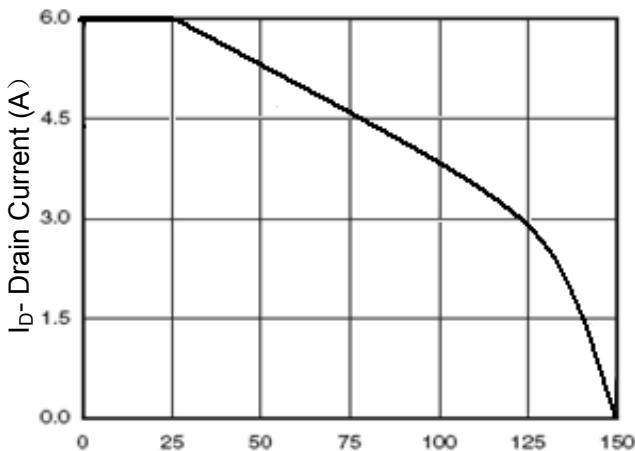


Figure 2:Switching Waveforms



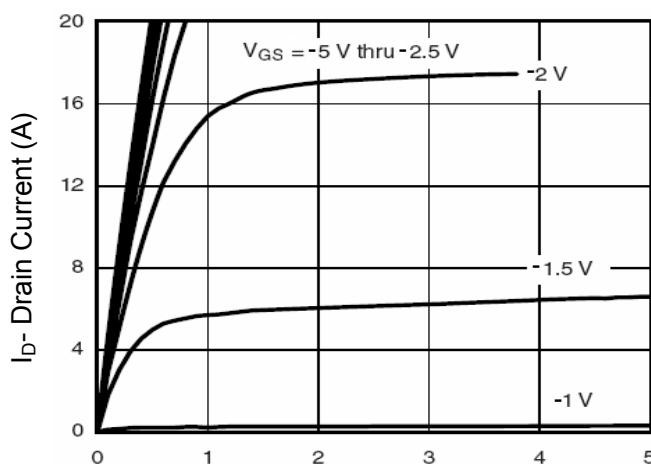
T<sub>J</sub>-Junction Temperature(°C)

Figure 3 Power Dissipation



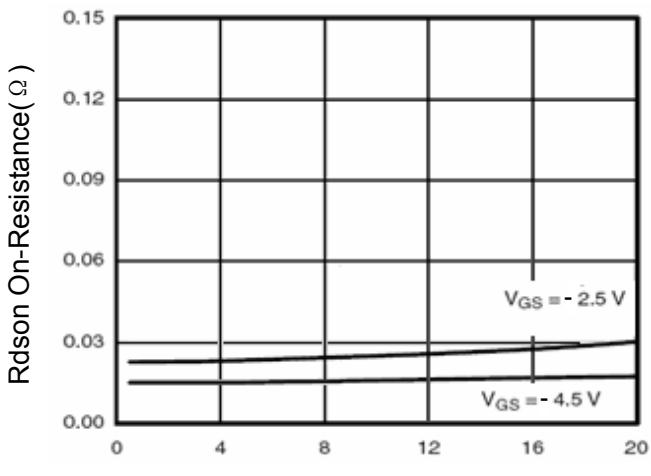
T<sub>J</sub>-Junction Temperature(°C)

Figure 4 Drain Current



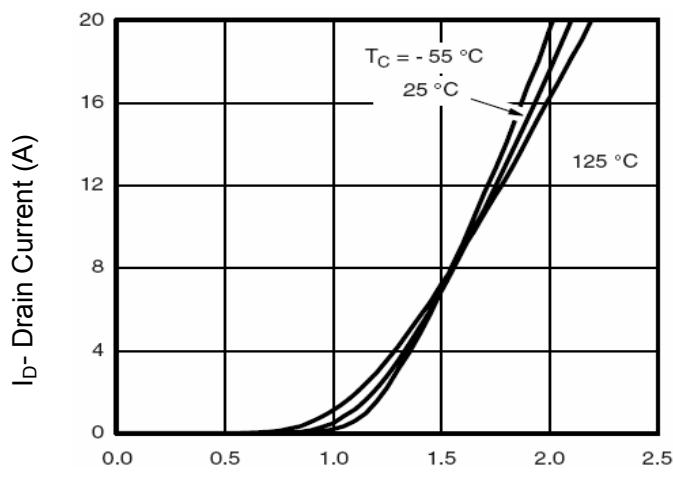
Vds Drain-Source Voltage (V)

Figure 5 Output Characteristics

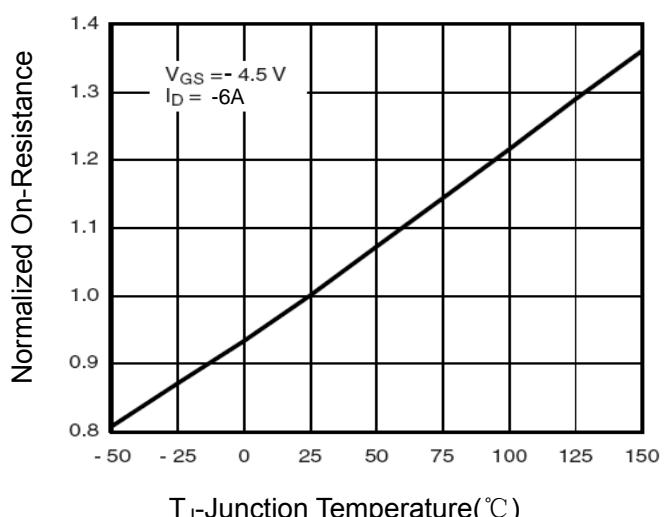


I<sub>D</sub> - Drain Current (A)

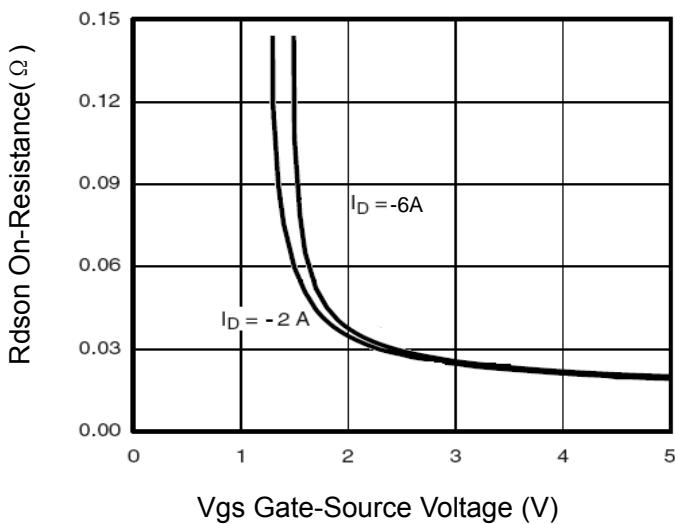
Figure 6 Drain-Source On-Resistance



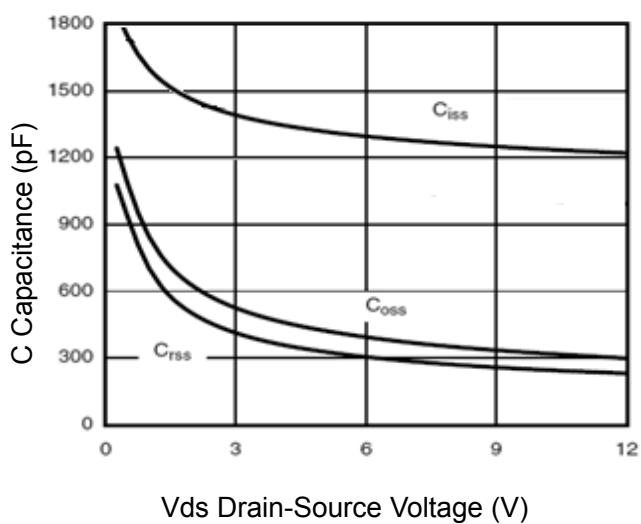
**Figure 7 Transfer Characteristics**



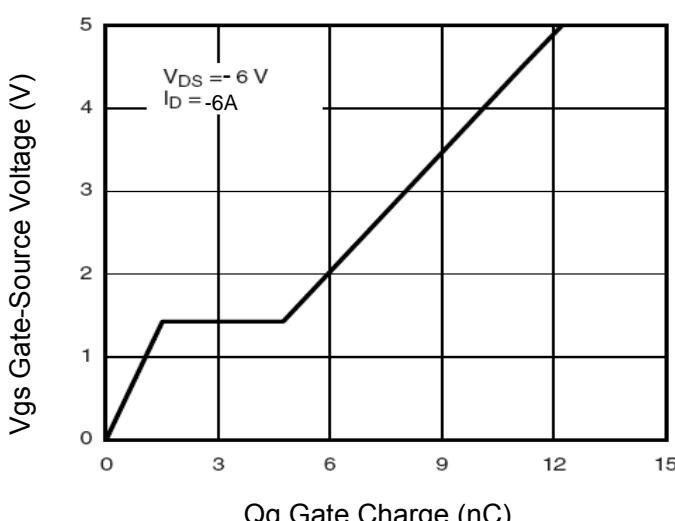
**Figure 8 Drain-Source On-Resistance**



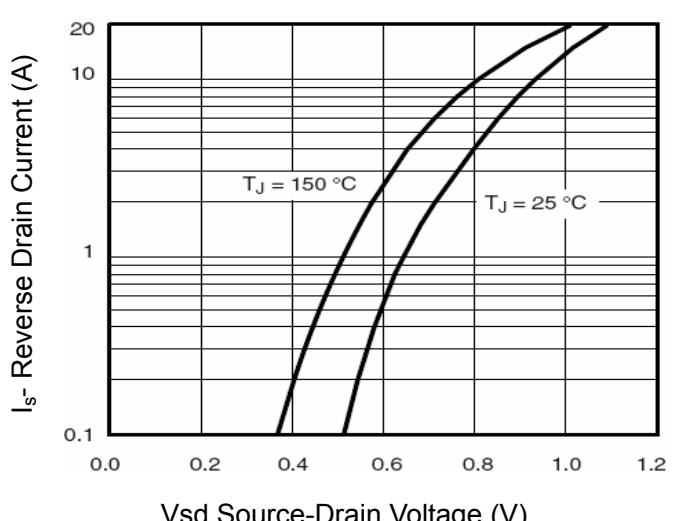
**Figure 9 Rdson vs Vgs**



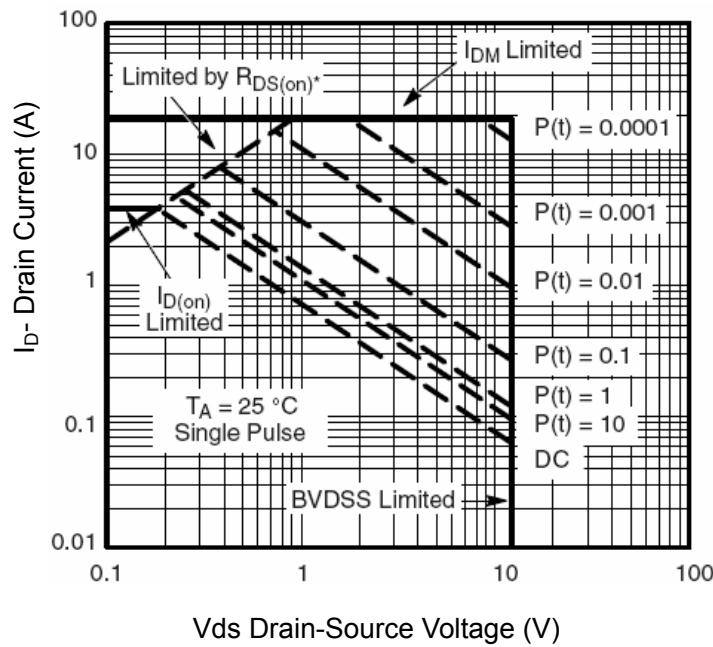
**Figure 10 Capacitance vs Vds**



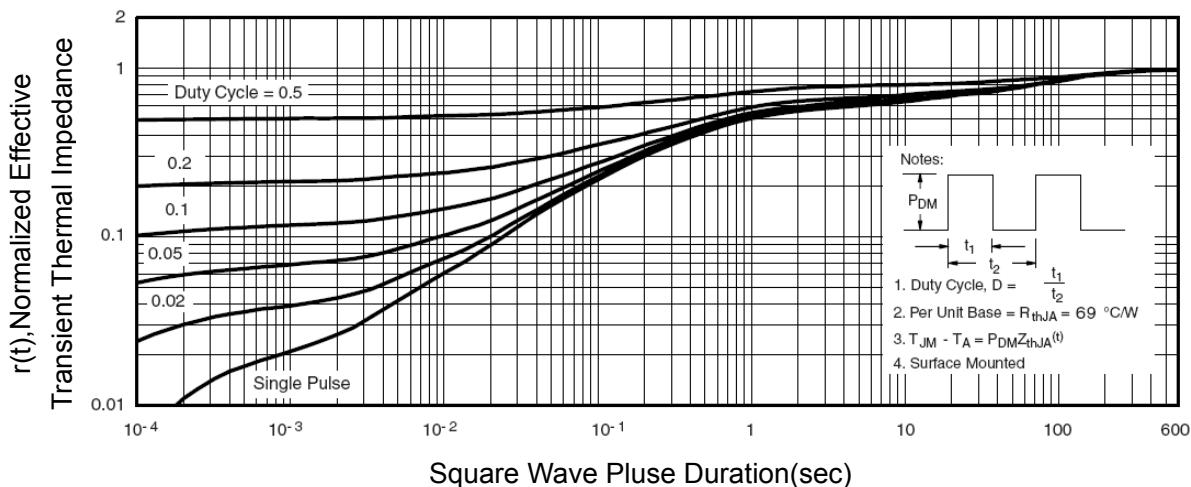
**Figure 11 Gate Charge**



**Figure 12 Source- Drain Diode Forward**

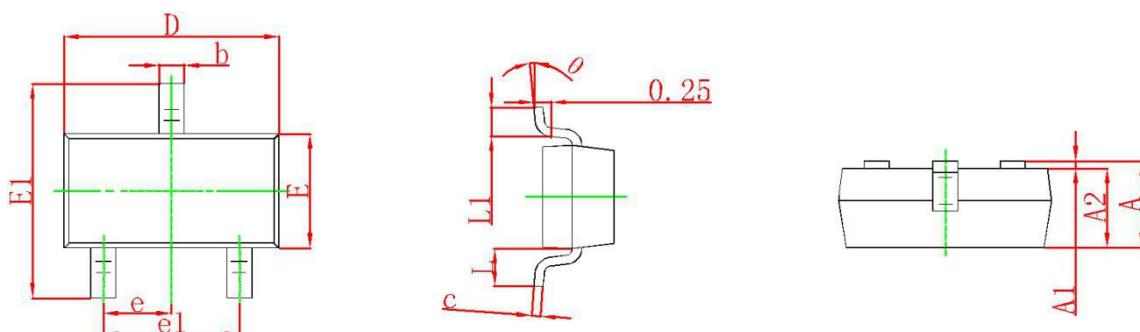


**Figure 13 Safe Operation Area**



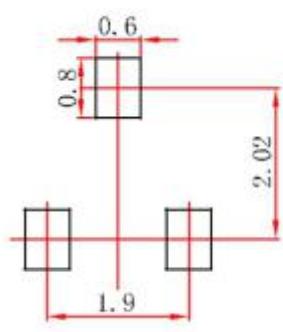
**Figure 14 Normalized Maximum Transient Thermal Impedance**

### SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

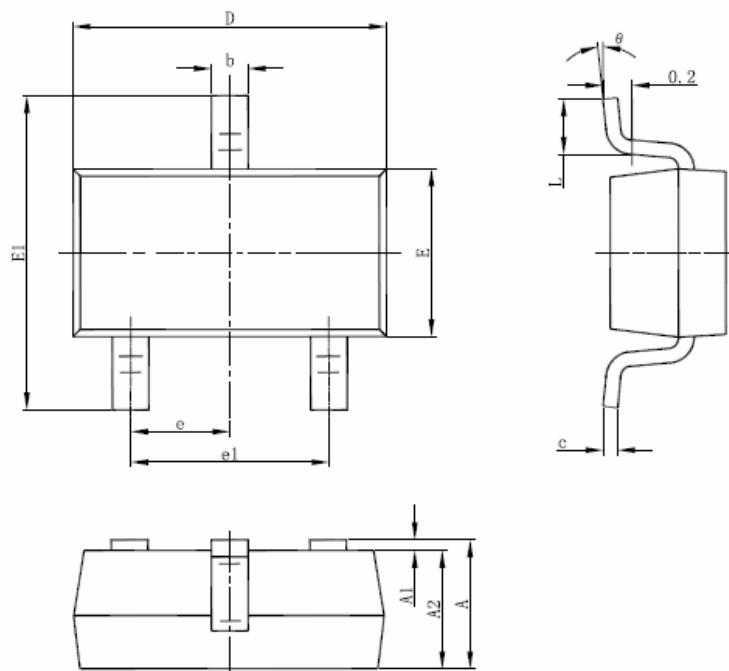
### SOT-23 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.

### SOT-23-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

### Notes

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.