



# SOP-8 Plastic-Encapsulate MOSFETS

**TF4407**

## P-Channel Enhancement Mode Power MOSFET

<b>Description</b> The TF4407 uses advanced trench technology to provide excellent $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V.	<b>SOP-8</b>  Top View <b>Equivalent Circuit</b>  <b>MARKING</b> 
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### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ )	$I_D$	-12	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-60	A
Maximum Power Dissipation	$P_D$	2.1	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ\text{C}$

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	50	$^\circ\text{C}/\text{W}$
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**SOP-8 Plastic-Encapsulate MOSFETS****TF4407****Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics</b> <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-2.0	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-12.0\text{A}$	-	9	12	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-7.0\text{A}$	-	13	17	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-5.0\text{A}$	20	-	-	S
<b>Dynamic Characteristics</b> <small>(Note 4)</small>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	1750	-	PF
Output Capacitance	$C_{\text{oss}}$		-	215	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	180	-	PF
<b>Switching Characteristics</b> <small>(Note 4)</small>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}, I_{\text{D}}=-10\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{GEN}}=1\Omega$	-	9	-	nS
Turn-on Rise Time	$t_r$		-	8	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	28	-	nS
Turn-Off Fall Time	$t_f$		-	10	-	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-10.0\text{A}, V_{\text{GS}}=-10\text{V}$	-	24	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	3.5	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	6	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <small>(Note 3)</small>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-10\text{A}$	-	-	-1.2	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

**Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

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## Typical Electrical and Thermal Characteristics

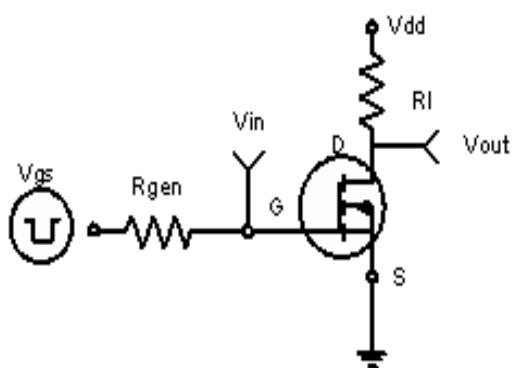


Figure 1:Switching Test Circuit

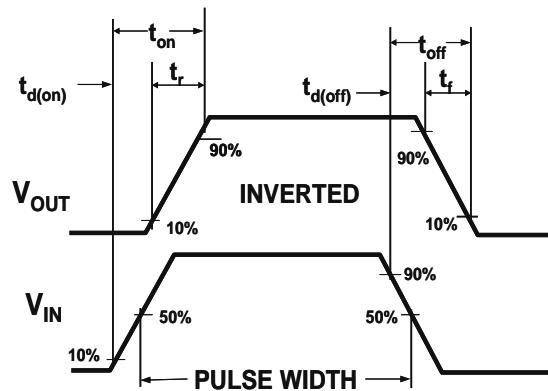
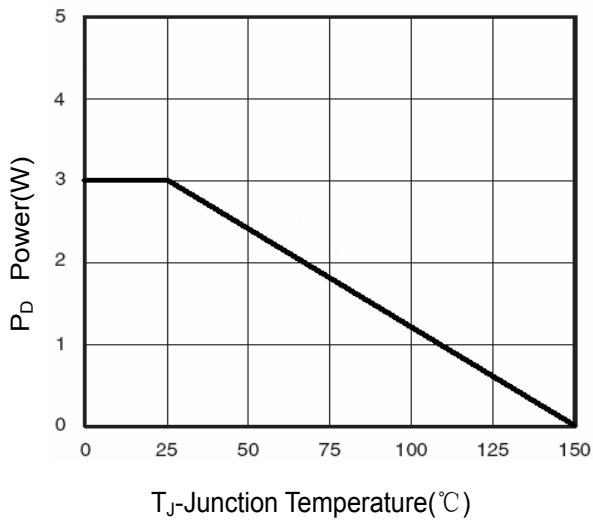
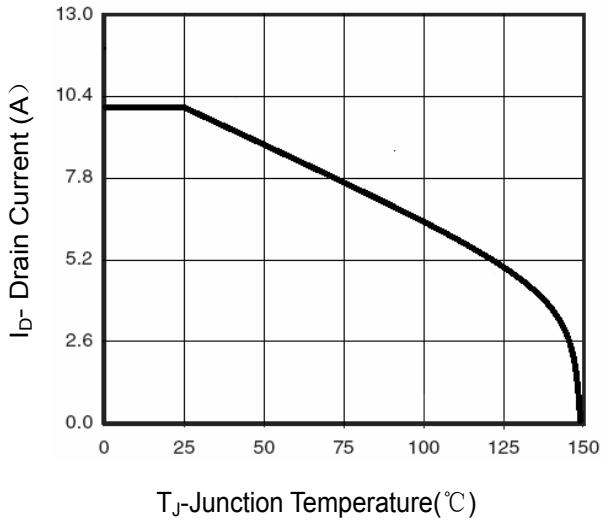


Figure 2:Switching Waveforms



T<sub>J</sub>-Junction Temperature(°C)

Figure 3 Power Dissipation



T<sub>J</sub>-Junction Temperature(°C)

Figure 4 Drain Current

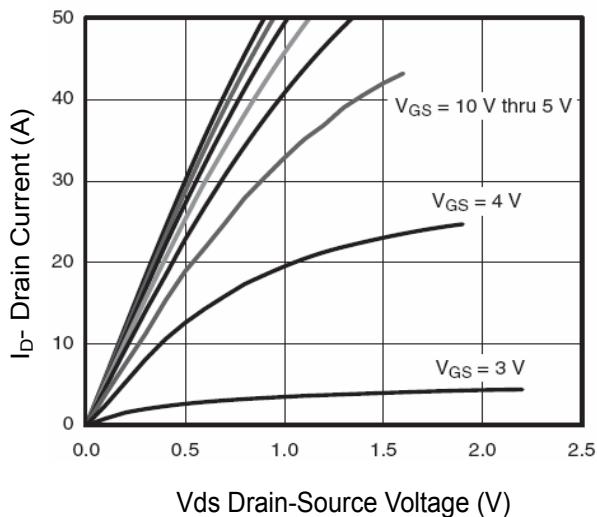


Figure 5 Output Characteristics

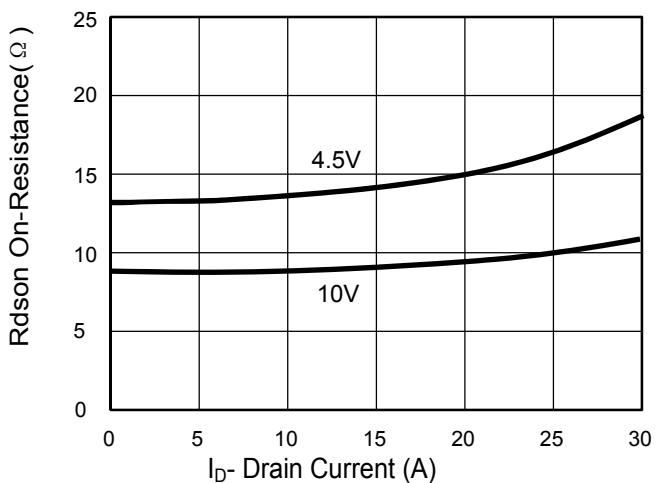
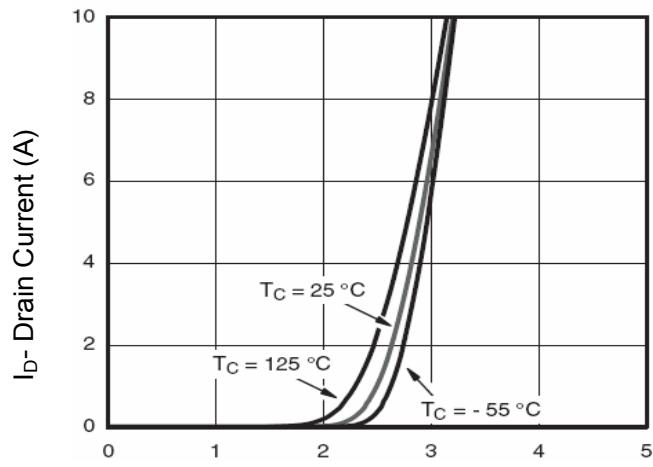


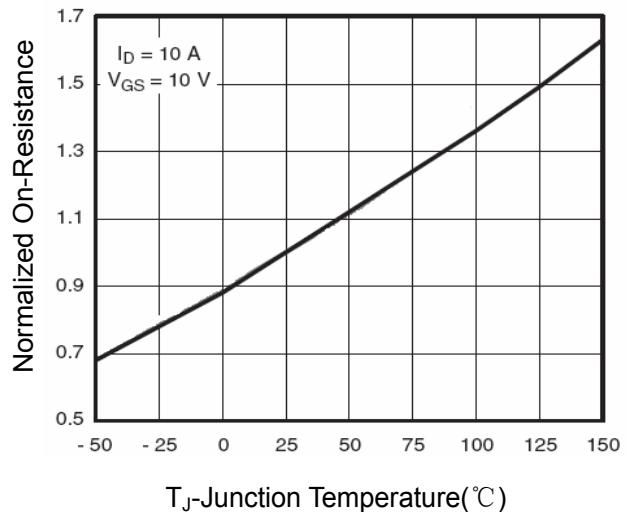
Figure 6 Drain-Source On-Resistance

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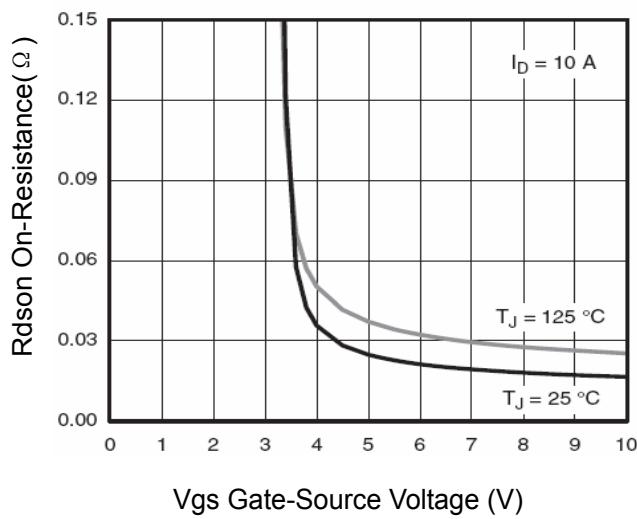
**TF4407**



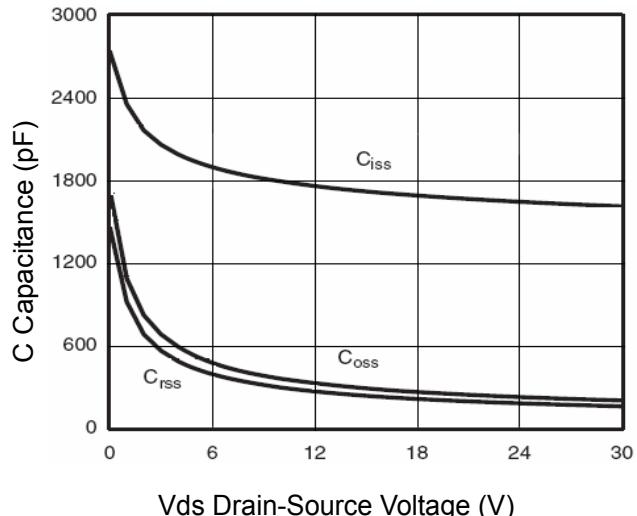
**Figure 7 Transfer Characteristics**



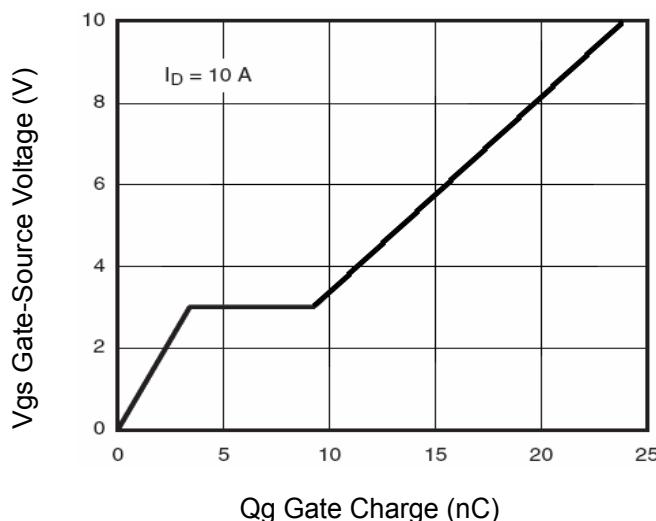
**Figure 8 Drain-Source On-Resistance**



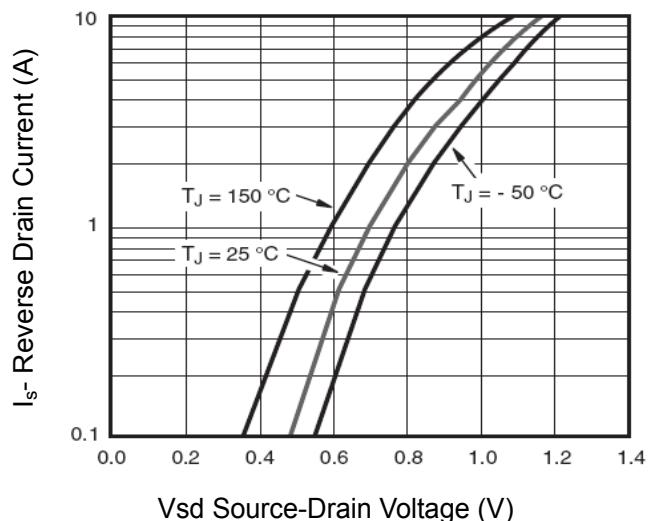
**Figure 9 Rdson vs Vgs**



**Figure 10 Capacitance vs Vds**



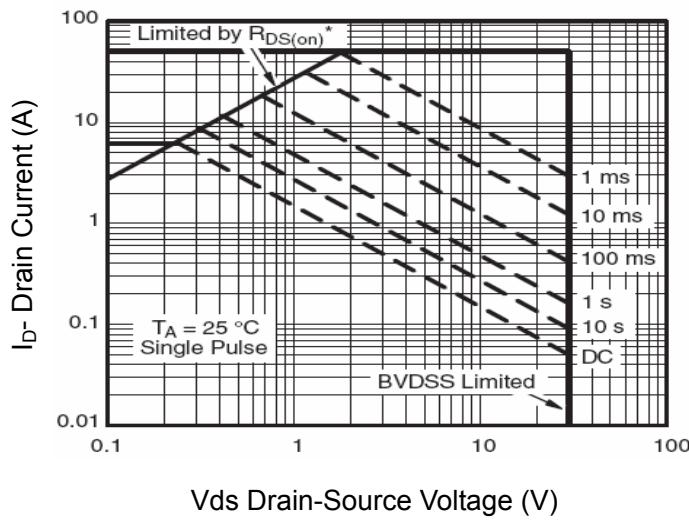
**Figure 11 Gate Charge**



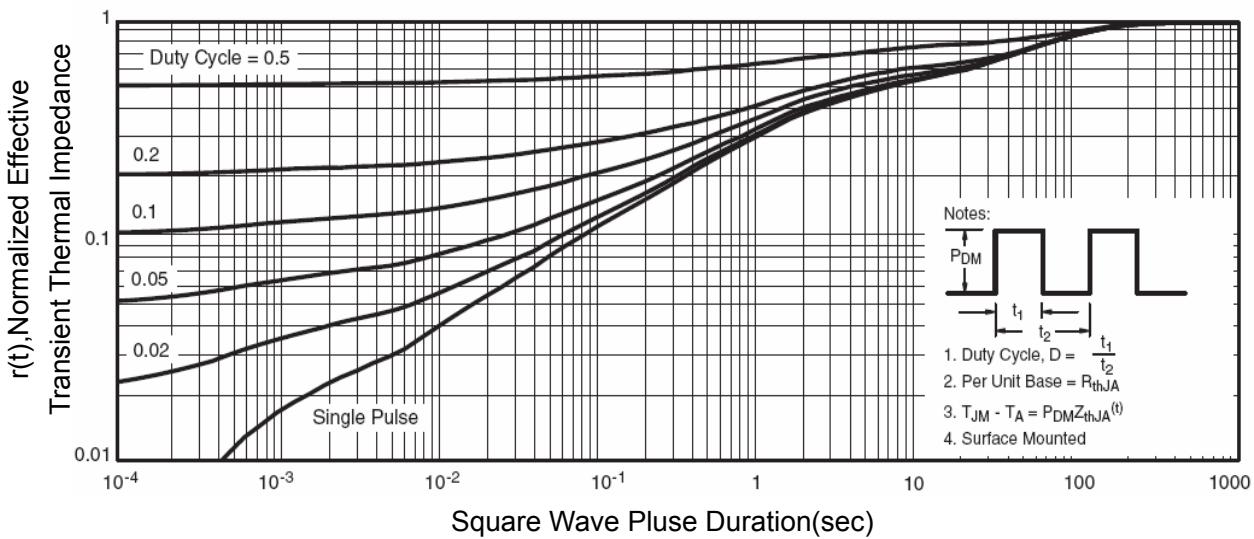
**Figure 12 Source-Drain Diode Forward**

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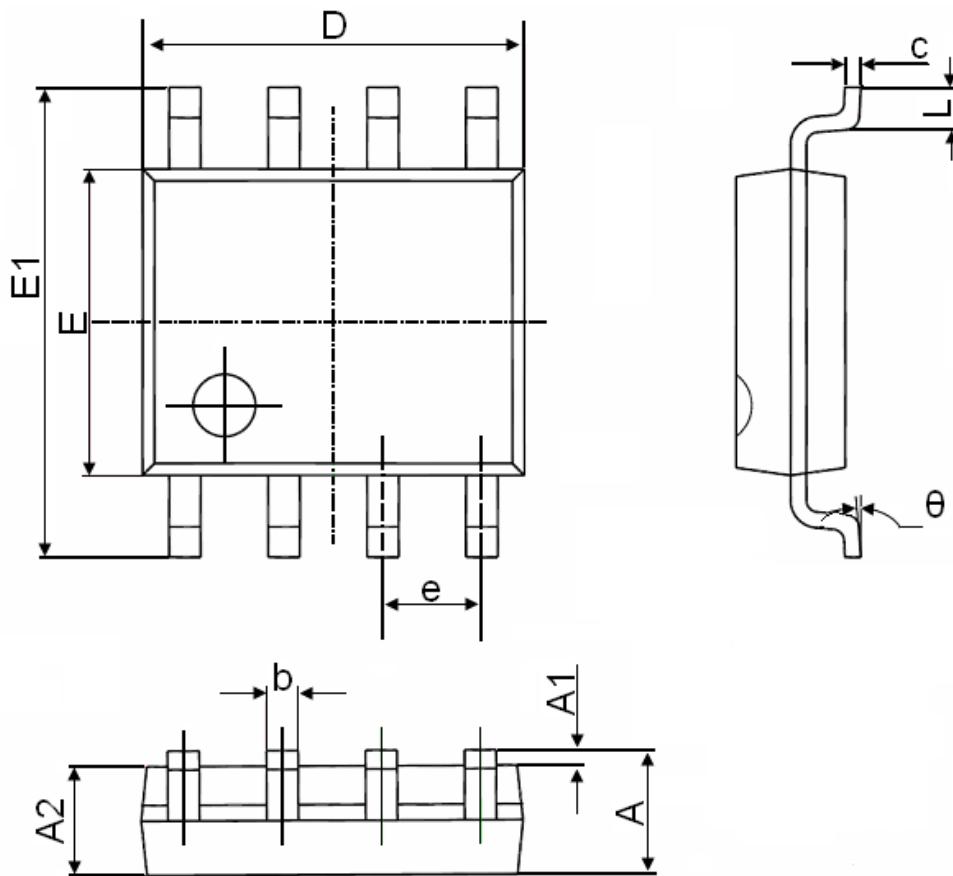
**TF4407**



**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**

**SOP-8 Plastic-Encapsulate MOSFETs****TF4407****SOP-8 Package Information**

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°