



Shenzhen Tuofeng Semiconductor Technology Co., Ltd

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

SGT MOS、低内阻、低结电容开关损耗小

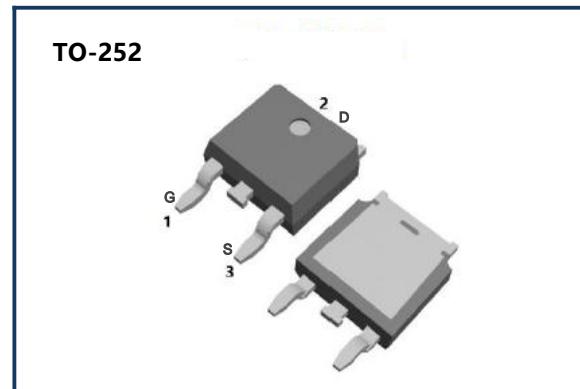
TF40N10KG

FEATURE

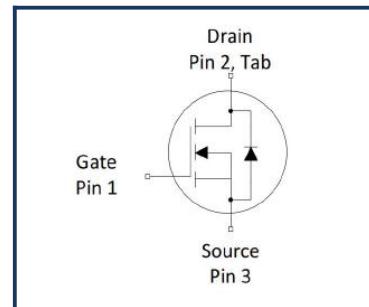
- ◆ Extremely low on-resistance $R_{DS(on)}$
- ◆ Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- ◆ Excellent Low Ciss
- ◆ Qualified according to JEDEC criteria
- ◆ High robustness and reliability
- ◆ Increases maximum current capability
- ◆ Low power loss, high power density
- ◆ Easy paralleling

Application

- ◆ Synchronous Rectification for AC/DC Quick Charger
- ◆ Battery management
- ◆ UPS(Uninterruptible Power Supplies)



Parameter	Values	Unit
Bvdss	100	V
Id	40	A
Rdson(typ)	23	mΩ
VGS(th) (typ)	1.2	V



Ordering Code	Marking	Package	Packaging
TF40N10KG	TF40N10KG	TO-252	Tape & Reel

Absolute Maximum Ratings($T_c=25^\circ\text{C}$,unless otherwise noted)

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Voltage	V_{DSS}	100	-	-	V	-
Gate-Source Voltage	V_{GS}	-20	-	20	V	-
Continuous Drain Current	I_D	-	-	40	A	$T_c=25^\circ\text{C}$ (Silicon limit)
		-	-	55		$T_c=25^\circ\text{C}$ (Package limit)
		-	-	25		$T_c=100^\circ\text{C}$ (Silicon limit)
		-	-	5		$T_a=25^\circ\text{C}$
Pulsed Drain Current(Note1)	I_{DM}	-	-	120	A	$T_c = 25^\circ\text{C}, t_p = 100\mu\text{s}$
Single Pulse Avalanche Energy	E_{AS}	-	-	50	mJ	$L=0.5\text{mH}, V_D=50\text{V}, T_c=25^\circ\text{C}$
Power Dissipation	P_D	-	-	25	W	$T_c=25^\circ\text{C}$
		-	-	1.1		$T_a=25^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55	-	150	°C	-
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	-	-	260	°C	-



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Thermal Characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Thermal resistance , junction – case	R _{thJC}	-	8.9	-	°C/W	-
Thermal resistance junction - ambient(min.footprint)	R _{thJA}	-	-	110		-

Electrical Characteristics ($T_c=25^\circ\text{C}$,unless otherwise noted)

Static characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Drain-Source Breakdown Voltage	BV _{DSS}	100	-	-	V	V _{GS} =0V,I _D =250μA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	V _{DS} =100V,V _{GS} =0V,T _j =25°C
		-	-	100		V _{DS} =100V,V _{GS} =0V,T _j =150°C
Gate-Body Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} =±20V,V _{DS} =0V
Gate-Source Threshold Voltage	V _{GS(th)}	1.0	1.2	1.7	V	V _{DS} =V _{GS} ,I _D =250μA
Drain-Source On-State Resistance	R _{DS(on)}	-	23	27	mΩ	V _{GS} =10V,I _D =20A
		-	28	34		V _{GS} =4.5V,I _D =10A
Gate Resistance	R _g	-	0.6	-	Ω	V _{GS} =0V,V _{DS} =0V,f=1MHz
Forward Transconductance	g _f	-	12	-	S	V _{DS} =5V,I _D =8A

Dynamic characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Input Capacitance	C _{iss}	-	609	-	pF	V _{DS} =50V,V _{GS} =0V,f=1.0MHZ
Output Capacitance	C _{oss}	-	380	-		
Reverse Transfer Capacitance	C _{rss}	-	29	-		
Turn-On Delay Time	t _{d(on)}	-	8.5	-	ns	V _{DD} =50V,I _D =20A,V _{GS} =10V,R _{G_ext} =1.6Ω
Turn-On Rise Time	t _r	-	47	-		
Turn-Off Delay Time	t _{d(off)}	-	75	-		
Turn-Off Fall Time	t _f	-	0.7	-		

Gate charge characteristics

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Total Gate Charge	Q _g	-	14	-	nC	V _{DS} =50V,I _D =20A,V _{GS} =10V
Gate-Source Charge	Q _{gs}	-	3.5	-		
Gate-Drain Charge	Q _{gd}	-	2.7	-		



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Reverse diode

Parameter	Symbol	Values			Unit	Note/Test Conditions
		Min	Typ	Max		
Continuous Diode Forward Current	I_S	-	-	40	A	$T_C = 25^\circ C$
Pulsed Diode Forward Current	I_{SM}	-	-	121	A	$T_C = 25^\circ C$
Diode Forward Voltage	V_{SD}	-	-	1.2	V	$I_S=20A, V_{GS}=0V$
Reverse Recovery Time	t_{rr}	-	47	-	ns	$I_F=20A$
Reverse Recovery Charge	Q_{rr}	-	32	-	nC	$di/dt=100A/\mu s$



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Fig.1 Power Dissipation

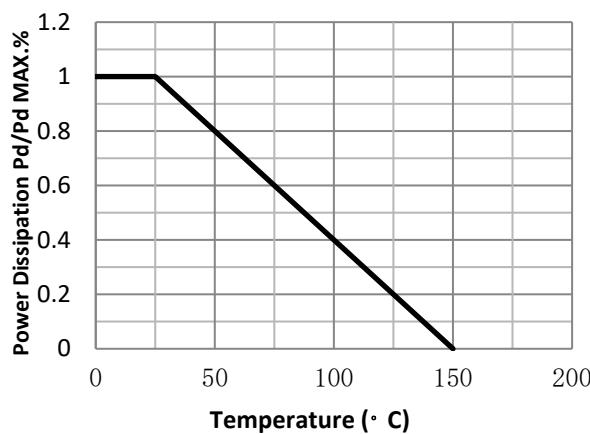


Fig.2 Typical output Characteristics

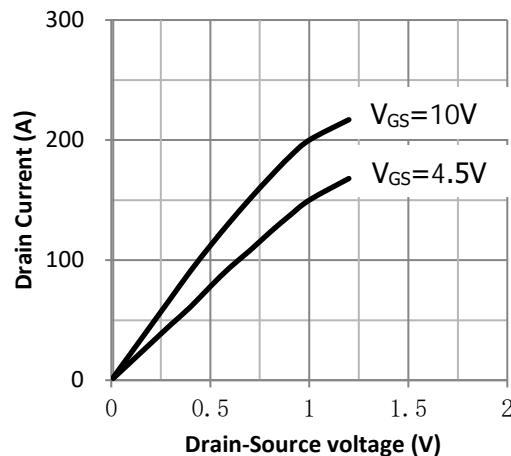


Fig.3 Threshold Voltage V.S Junction Temperature

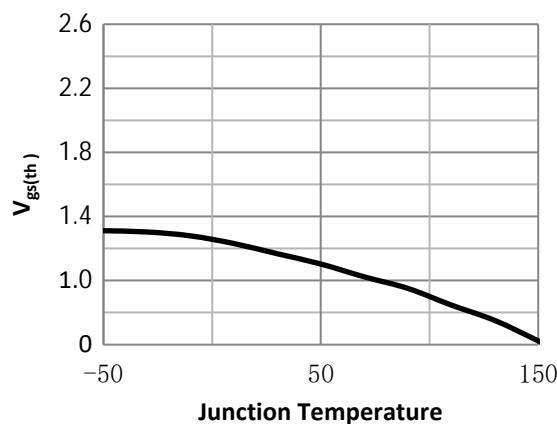


Fig.4 Resistance V.S Drain Current

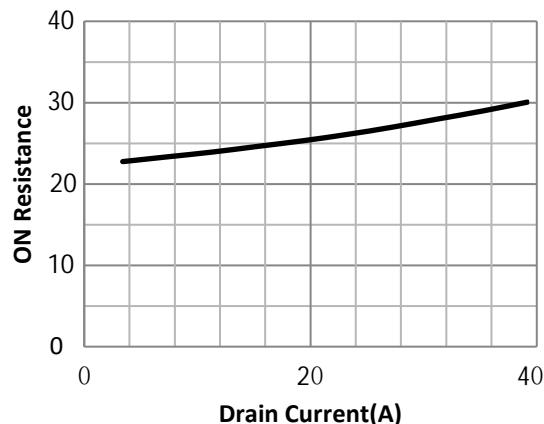


Fig.5 On-Resistance VS Drain Current

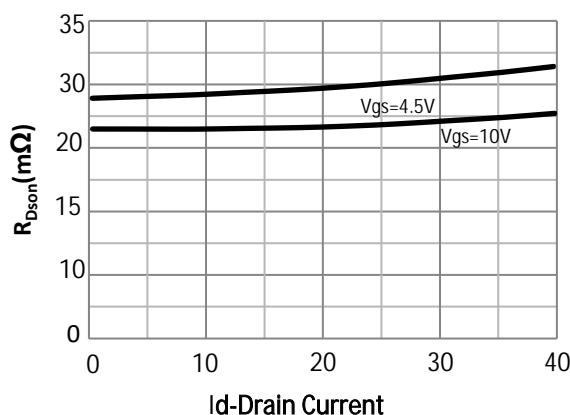


Fig.6 On-Resistance V.S Junction Temperature

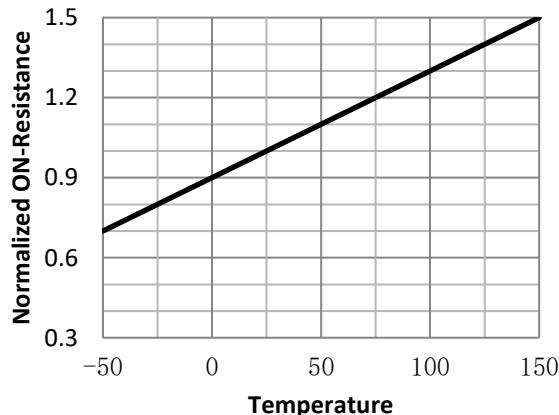


Fig.7 Switching Time Measurement Circuit

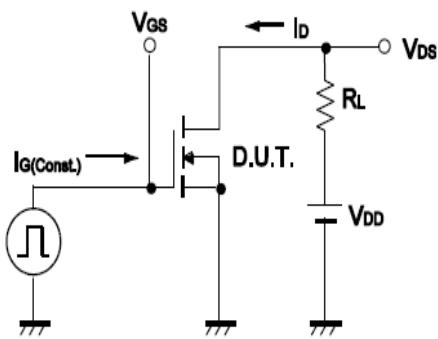


Fig.8 Gate Charge Waveform

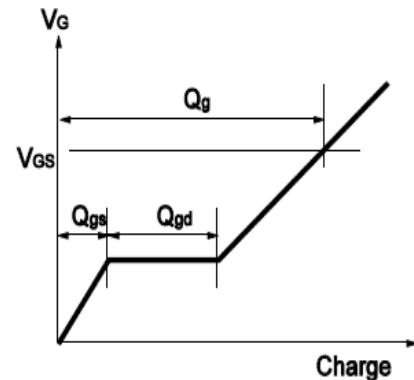


Fig.9 Switching Time Measurement Circuit

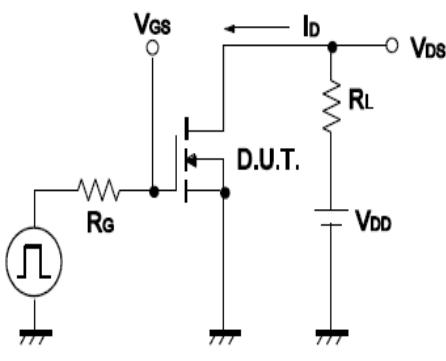


Fig.10 Gate Charge Waveform

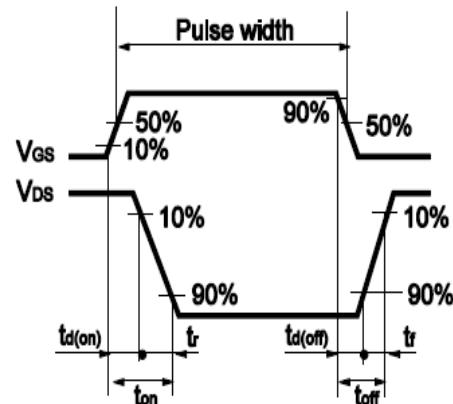


Fig.11 Avalanche Measurement Circuit

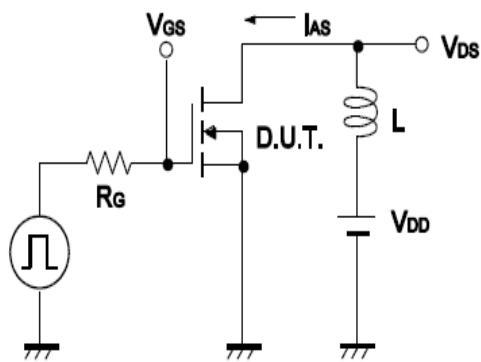
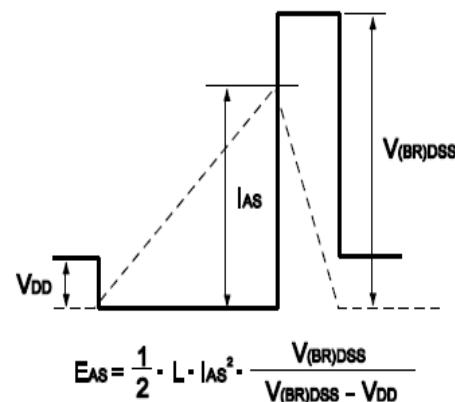


Fig.12 Avalanche Waveform





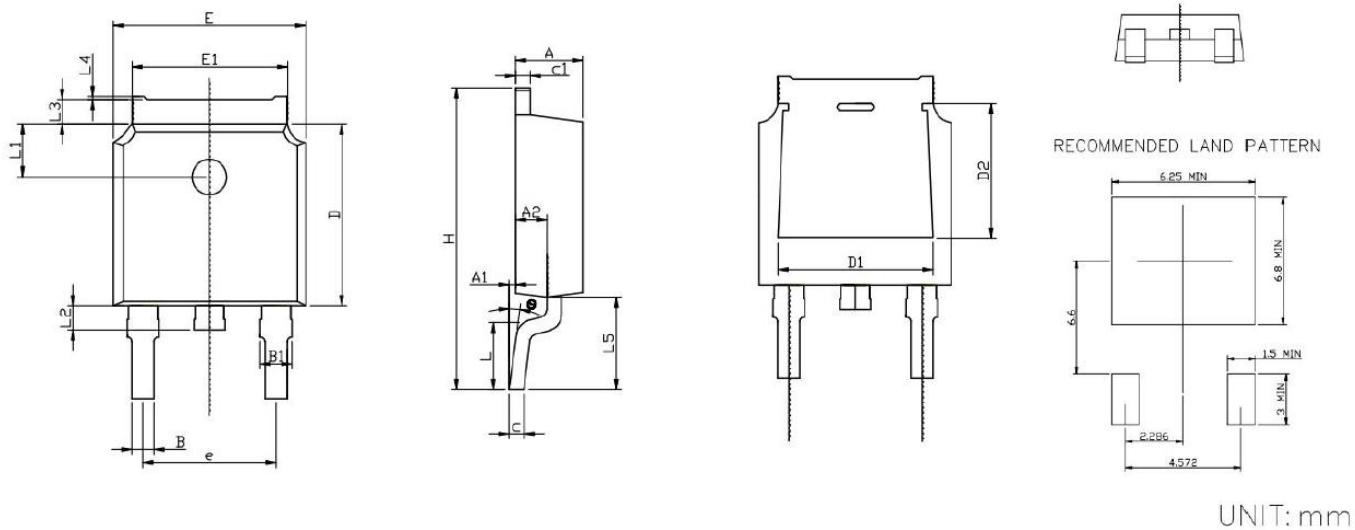
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Package Outline: TO-252



UNIT: mm

SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.15	2.45	0.085	0.096
A1	0.05	0.20	0.002	0.008
A2	0.91	1.22	0.036	0.048
B	0.66	0.86	0.026	0.034
B1	0.93	1.23	0.037	0.048
C	0.40	0.60	0.016	0.024
C1	0.40	0.60	0.016	0.024
D	5.95	6.25	0.234	0.246
D1	4.80		0.189	
D2	3.80		0.150	
E	6.45	6.75	0.254	0.266
E1	5.12	5.52	0.202	0.217
L	1.65		0.065	
L1	1.58	1.98	0.062	0.078
L2	0.60	1.00	0.024	0.039
L3	0.70	1.00	0.028	0.039
L4	0.00	0.20	0.000	0.008
L5	2.80	3.40	0.110	0.134
H	9.80	10.40	0.386	0.409
theta	0.00	8.00	0.000	0.315
e	4.57		0.180	