

N and P-Channel Enhancement Mode Power MOSFET

Description

The TF4614 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

General Features

● N-Channel

$$V_{DS} = 40V, I_D = 8.0A$$

$$R_{DS(ON)} (typ) < 12m\Omega @ V_{GS}=10V$$

$$R_{DS(ON)} (typ) < 16m\Omega @ V_{GS}=4.5V$$

● P-Channel

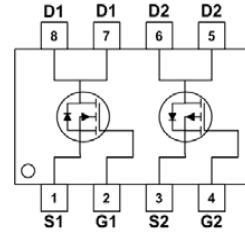
$$V_{DS} = -40V, I_D = -7.0A$$

$$R_{DS(ON)} (typ) < 32m\Omega @ V_{GS}=-10V$$

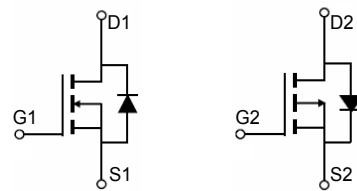
$$R_{DS(ON)} (typ) < 43m\Omega @ V_{GS}=-4.5V$$

- High power and current handling capability
- Lead free product is acquired
- Surface mount package

SOP-8L



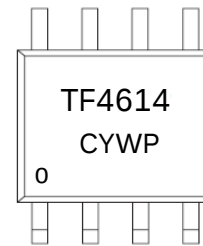
Schematic diagram



N-channel

P-channel

Marking and pin assignment



Y:year code W :week code

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	40	-40	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current	I_D	8.0	-7.0	A
Pulsed Drain Current ^(Note 1)	I_{DM}	40	-30	A
Maximum Power Dissipation	P_D	2.0	2.0	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	-55 To 150	$^\circ\text{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note2)	$R_{\theta JA}$	N-Ch	62.5	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient ^(Note2)	$R_{\theta JA}$	P-Ch	62.5	$^\circ\text{C/W}$

N-CH Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=40V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

On Characteristics (Note 3)

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=8A$	-	12	20	m Ω
		$V_{GS}=4.5V, I_D=6A$	-	16	25	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=8A$	15	-	-	S

Dynamic Characteristics (Note4)

Input Capacitance	C_{iss}	$V_{DS}=20V, V_{GS}=0V,$ $F=1.0MHz$	-	1314	-	PF
Output Capacitance	C_{oss}		-	95.4	-	PF
Reverse Transfer Capacitance	C_{rss}		-	78.5	-	PF

Switching Characteristics (Note 4)

Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=20V, R_L=2.0\Omega$ $V_{GS}=10V, R_G=3.0\Omega$	-	12.6	-	nS
Turn-on Rise Time	t_r		-	3.6	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	30.8	-	nS
Turn-Off Fall Time	t_f		-	3.2	-	nS
Total Gate Charge	Q_g	$V_{DS}=20V, I_D=8A,$ $V_{GS}=10V$	-	24.5	-	nC
Gate-Source Charge	Q_{gs}		-	3.7	-	nC
Gate-Drain Charge	Q_{gd}		-	6.2	-	nC

Drain-Source Diode Characteristics

Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=8A$	-	0.75	1.0	V
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Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

N- Channel Typical Electrical and Thermal Characteristics (Curves)

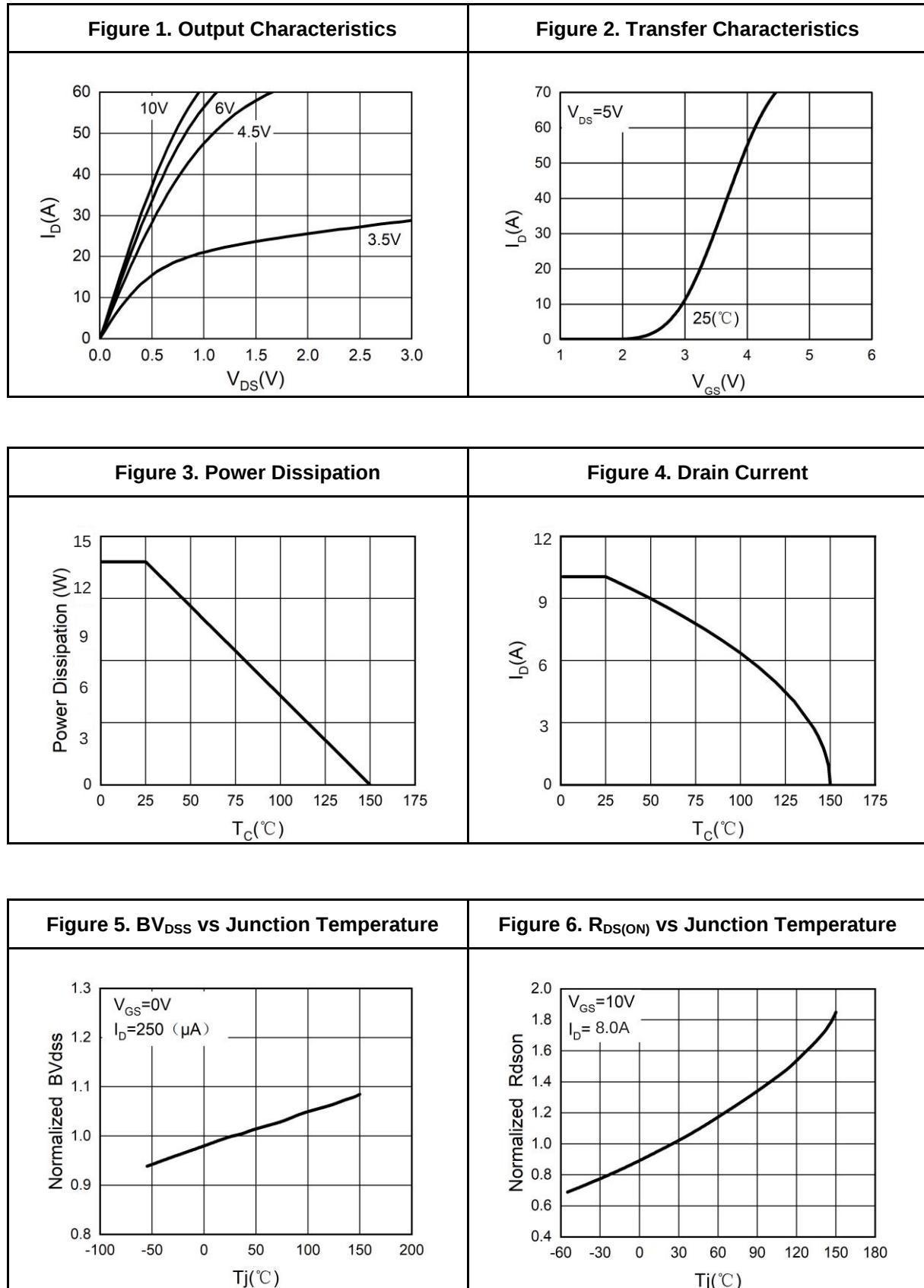


Figure 7. Gate Charge Waveforms

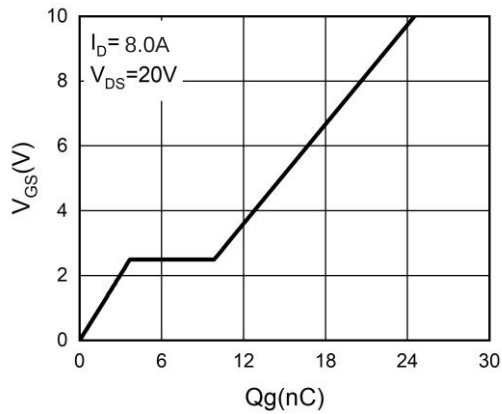


Figure 8. Capacitance

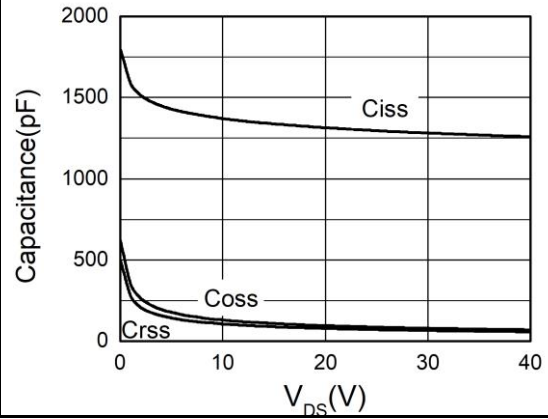


Figure 9. Body-Diode Characteristics

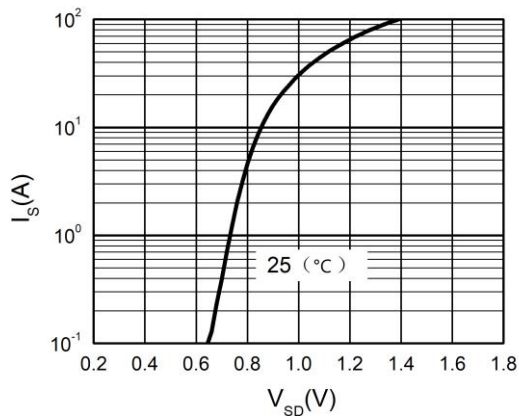
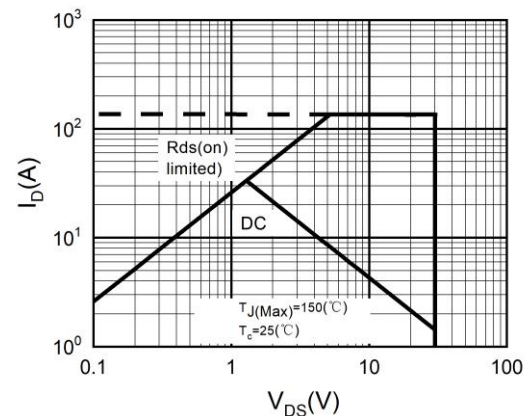


Figure 10. Maximum Safe Operating Area



SOP-8 Plastic-Encapsulate MOSFETS

TF4614

P-CH Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-40V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

On Characteristics (Note 3)

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.1	-1.7	-2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-7.0A$	-	32	35	m Ω
		$V_{GS}=-4.5V, I_D=-4.0A$	-	43	48	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-7.0A$	15	-	-	S

Dynamic Characteristics (Note 4)

Input Capacitance	C_{iss}	$V_{DS}=-20V, V_{GS}=0V,$ $F=1.0MHz$	-	1021	-	PF
Output Capacitance	C_{oss}		-	63.6	-	PF
Reverse Transfer Capacitance	C_{rss}		-	48.6	-	PF

Switching Characteristics (Note 4)

Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-20V, R_L=2\Omega$ $V_{GS}=-10V, R_G=10\Omega$	-	13	-	nS
Turn-on Rise Time	t_r		-	16	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	180	-	nS
Turn-Off Fall Time	t_f		-	86	-	nS
Total Gate Charge	Q_g	$V_{DS}=-20V, I_D=-7.0A$ $V_{GS}=-10V$	-	19.3	-	nC
Gate-Source Charge	Q_{gs}		-	2.5	-	nC
Gate-Drain Charge	Q_{gd}		-	5.5	-	nC

Drain-Source Diode Characteristics

Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=-7.0A$	-	0.75	-1.0	V
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Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
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P- Channel Typical Electrical and Thermal Characteristics (Curves)

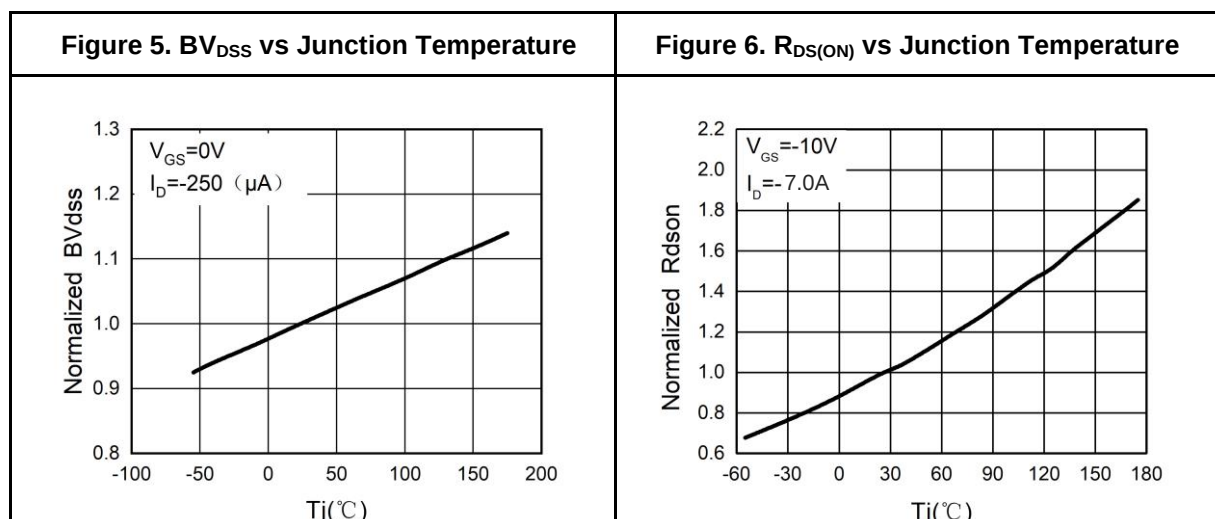
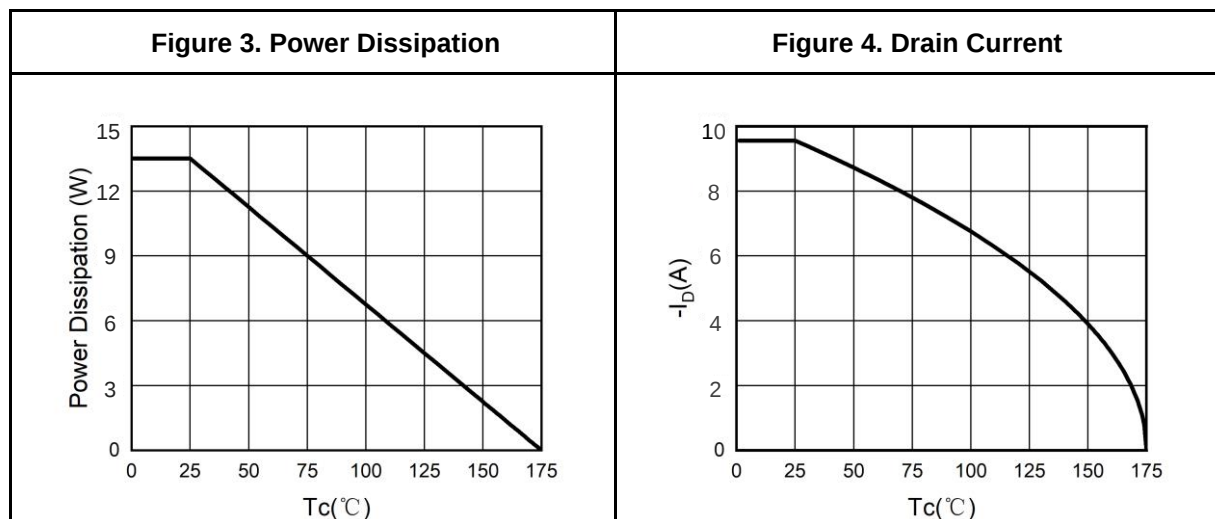
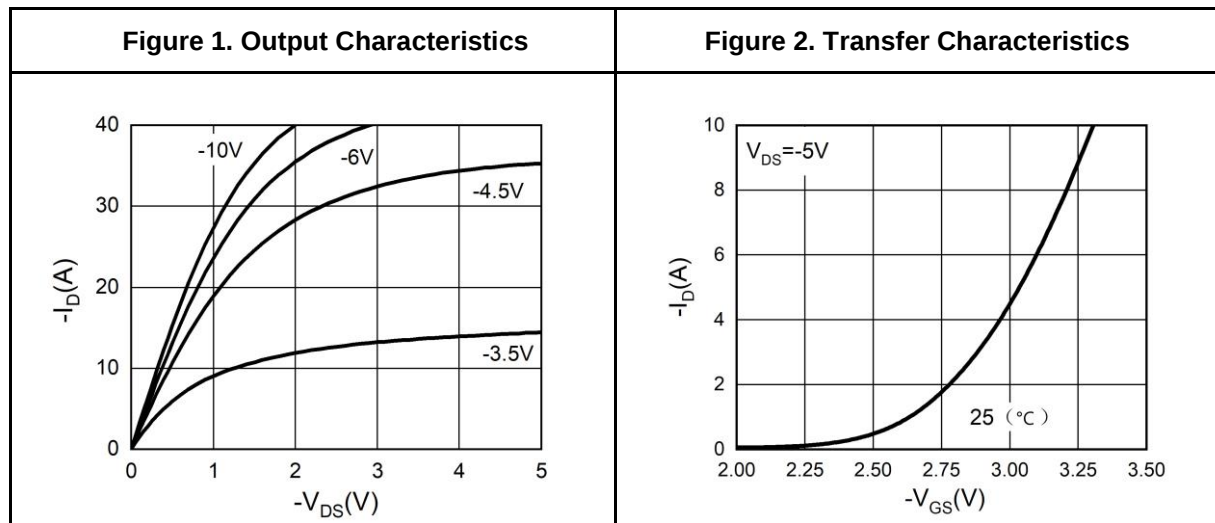


Figure 7. Gate Charge Waveforms

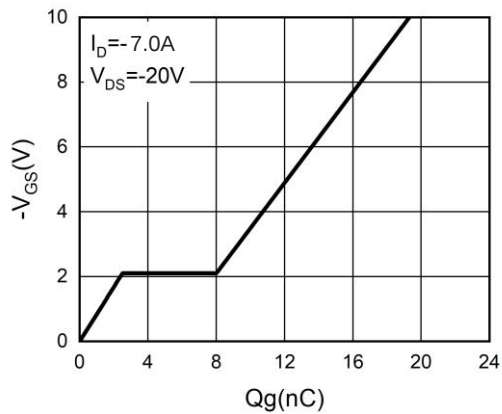


Figure 8. Capacitance

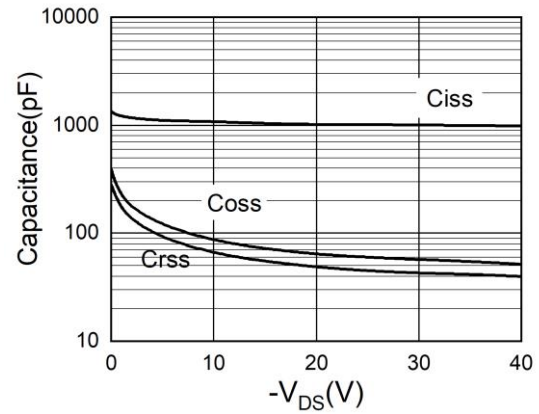


Figure 9. Body-Diode Characteristics

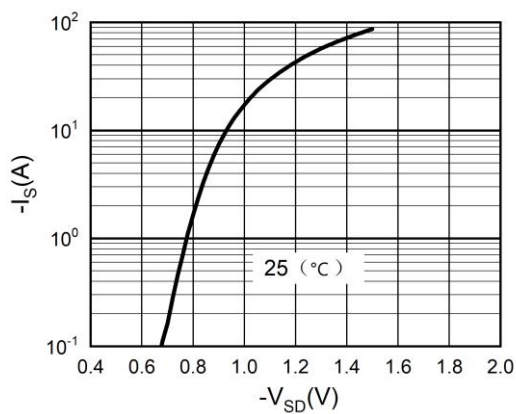
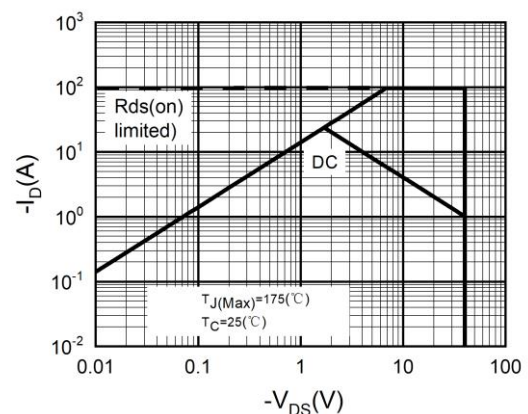
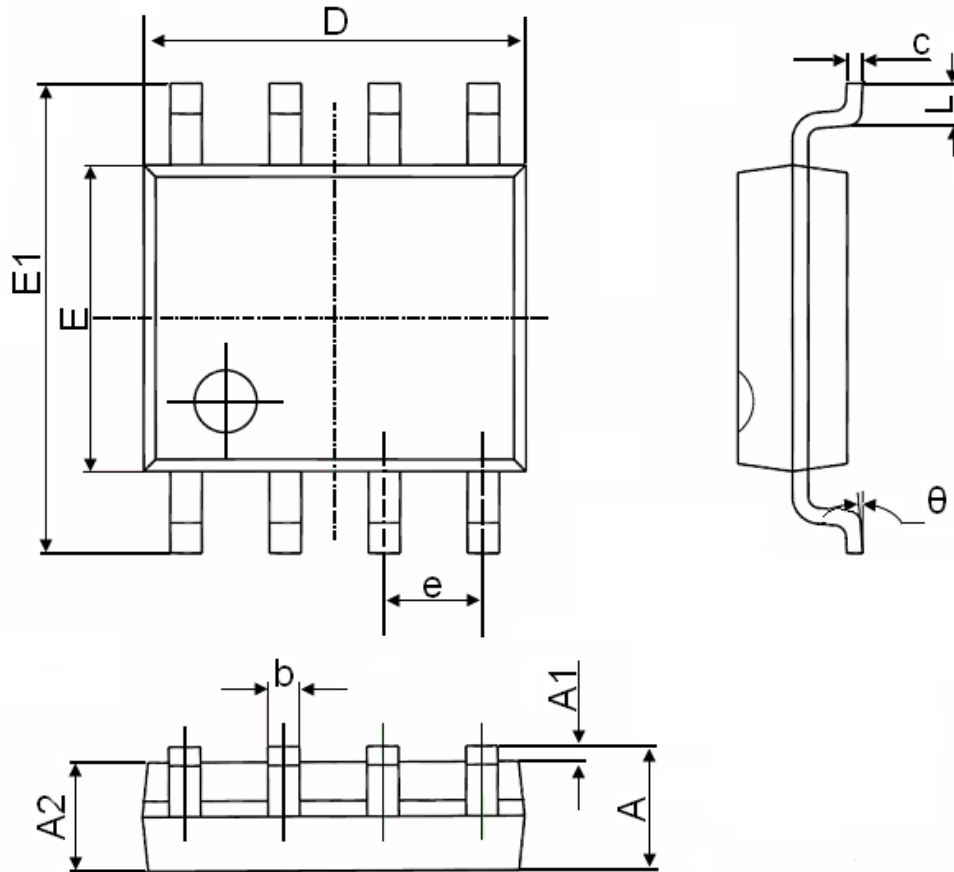


Figure 10. Maximum Safe Operating Area



SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°